

ABSTRACT OF THE DISCLOSURE

The present invention comprises a plurality of terminal-side apparatuses each having a buffer, a queue length notification module, and an output control module, and a network-side apparatus having a fairness guarantee/high efficiency hybrid time slot assignment control module (FG/HE HTSAC module). The FG/HE HTSAC module assigns time slots to all or a part of the terminal-side apparatuses in accordance with a fairness criteria. If the number of the time slots assigned exceeds a queue length for one or more terminal-side apparatuses, leftover time slots are reassigned among the terminal-side apparatus(es) where the number of the time slots assigned thereto does not exceed a queue length. The result of reassignment is notified to an output control circuit that controls output of cells from the buffer.